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J1046 U.S. PRO  
09/971136  
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	Subclass	ISSUE CLASSIFICATION
Class		

**PATENT NUMBER**

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**U.S. UTILITY Patent Application**

O.I.P.E.	PATENT DATE
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APPLICATION NO.	CONT/PRIOR	CLASS 427	SUBCLASS 134	ART UNIT 1762	EXAMINER <del>ELARIN</del> STINSON
09/971136	F		102.1	1746	

## APPlicants

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## TIME

## Substrate processing method and substrate processing apparatus

PTO-2040  
12/99

## **ISSUING CLASSIFICATION**

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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	(Assistant Examiner) _____ (Date)				
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	(Primary Examiner) _____ (Date)			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	(Legal Instruments Examiner) _____ (Date)				

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